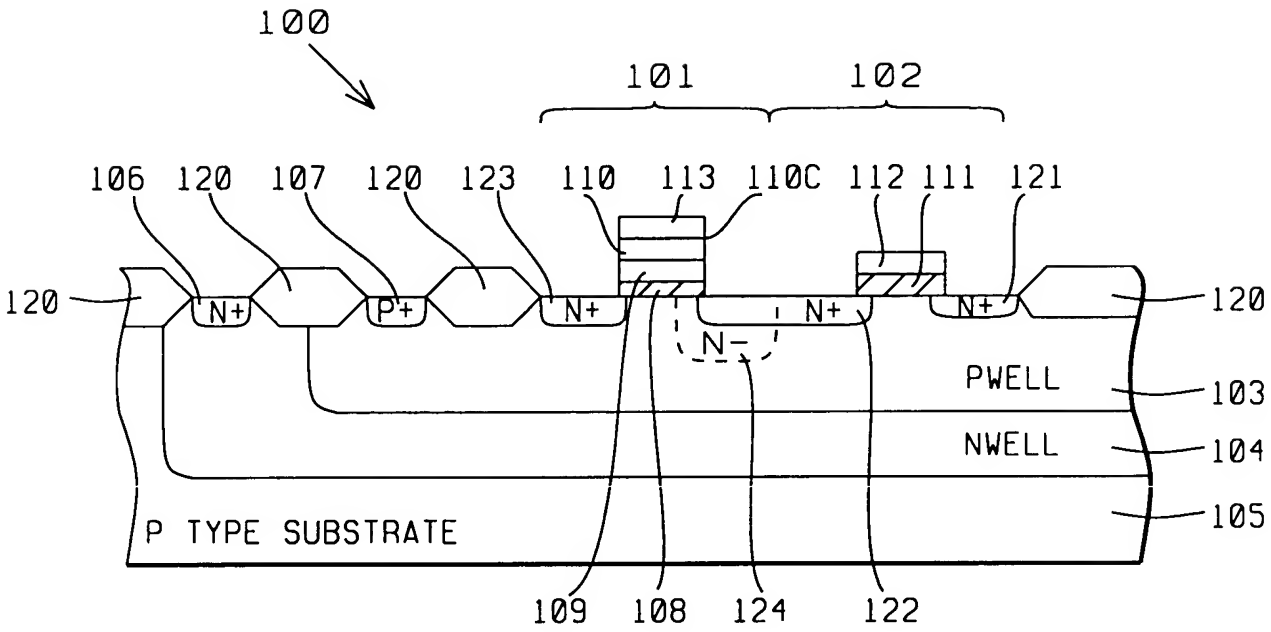

FIG. 1 – Prior Art

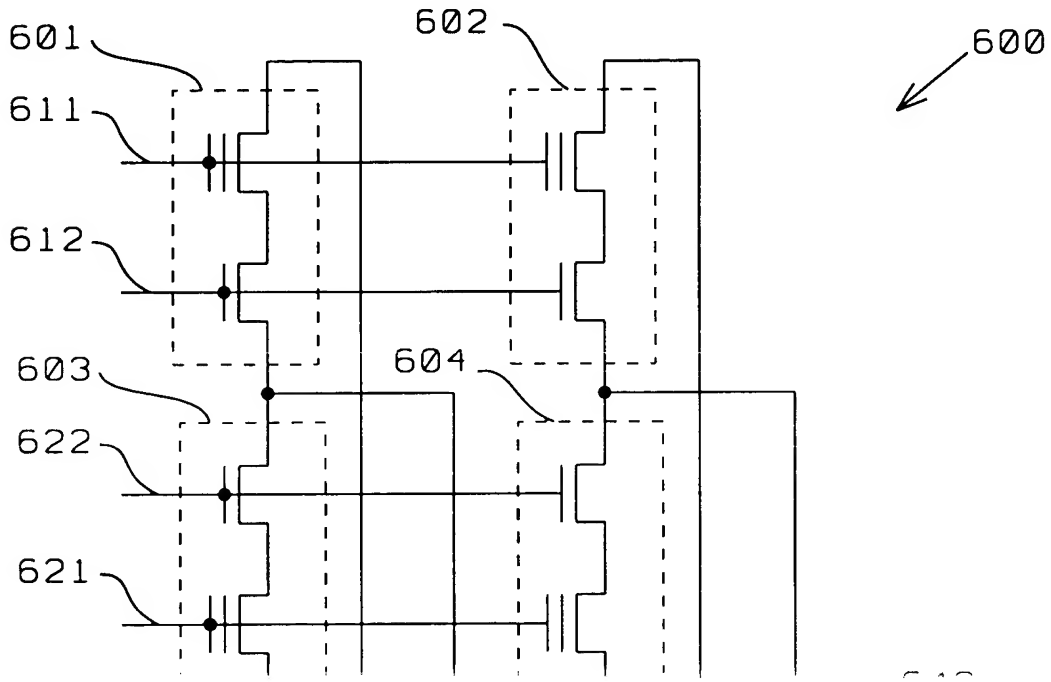

FIG. 2 – Prior Art

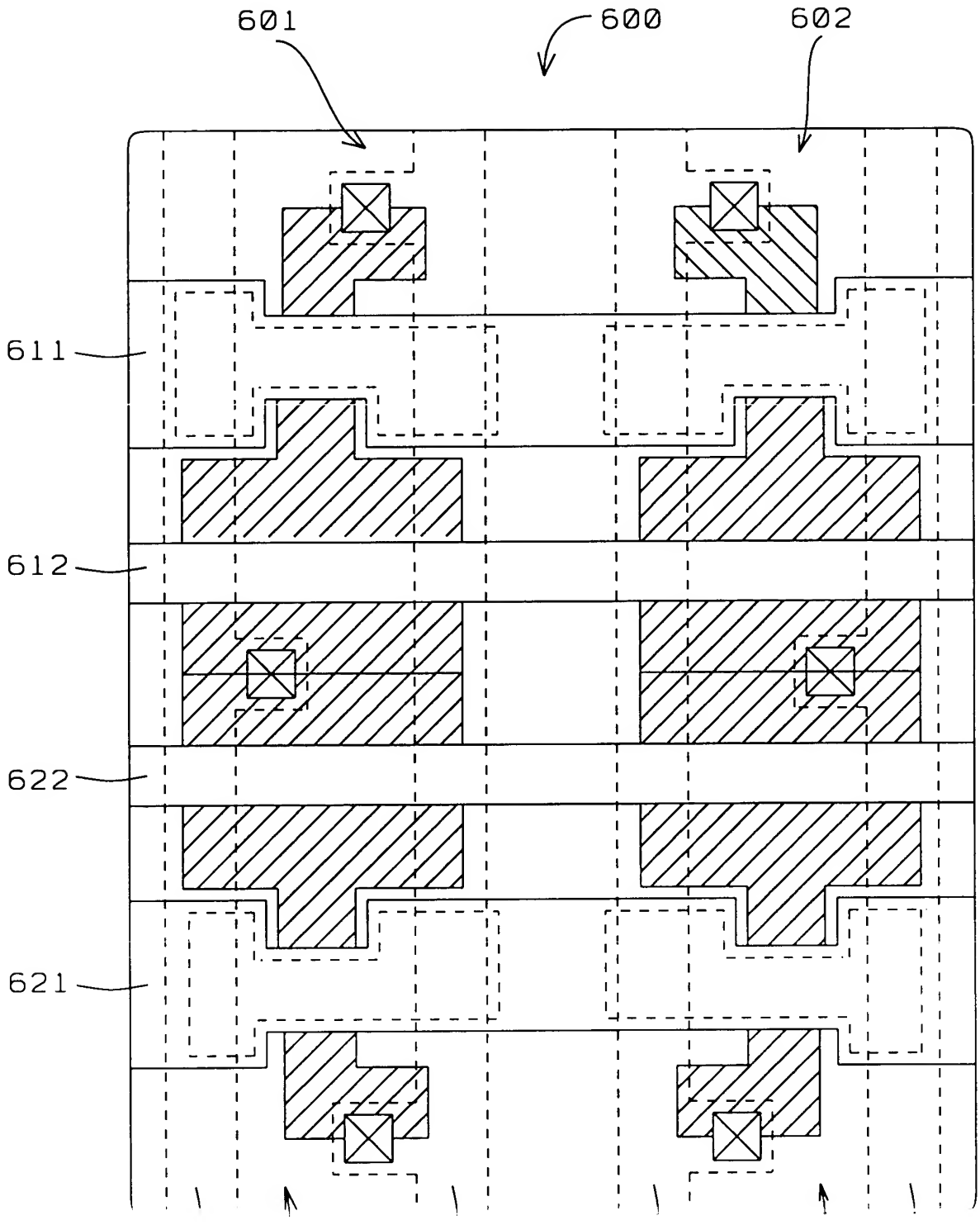

FIG. 3 – Prior Art

	Selected WL		Non-Selected WL					
MC	Vcg	Vag	Vcg	Vag	V_S	V_d	V_{pwell}	V_{nwell}
PROG	-7 TO -11 VOLTS	8 VOLTS	0 VOLTS	0 VOLTS	HIGH Z	5 TO 8 VOLTS	0 VOLTS	3.3 VOLTS
PROG INHI	-7 TO -11 VOLTS	8 VOLTS	0 VOLTS	0 VOLTS	HIGH Z	0 VOLTS	0 VOLTS	3.3 VOLTS
ERA	8 TO 10 VOLTS	0 VOLTS			-8 TO -10 VOLTS	HIGH Z	8 TO 10 VOLTS	3.3 VOLTS
RE	3.3 VOLTS	3.3 VOLTS	0 VOLTS	0 VOLTS	0 VOLTS	1 VOLT	0 VOLTS	3.3 VOLTS

FIG. 4 – Prior Art

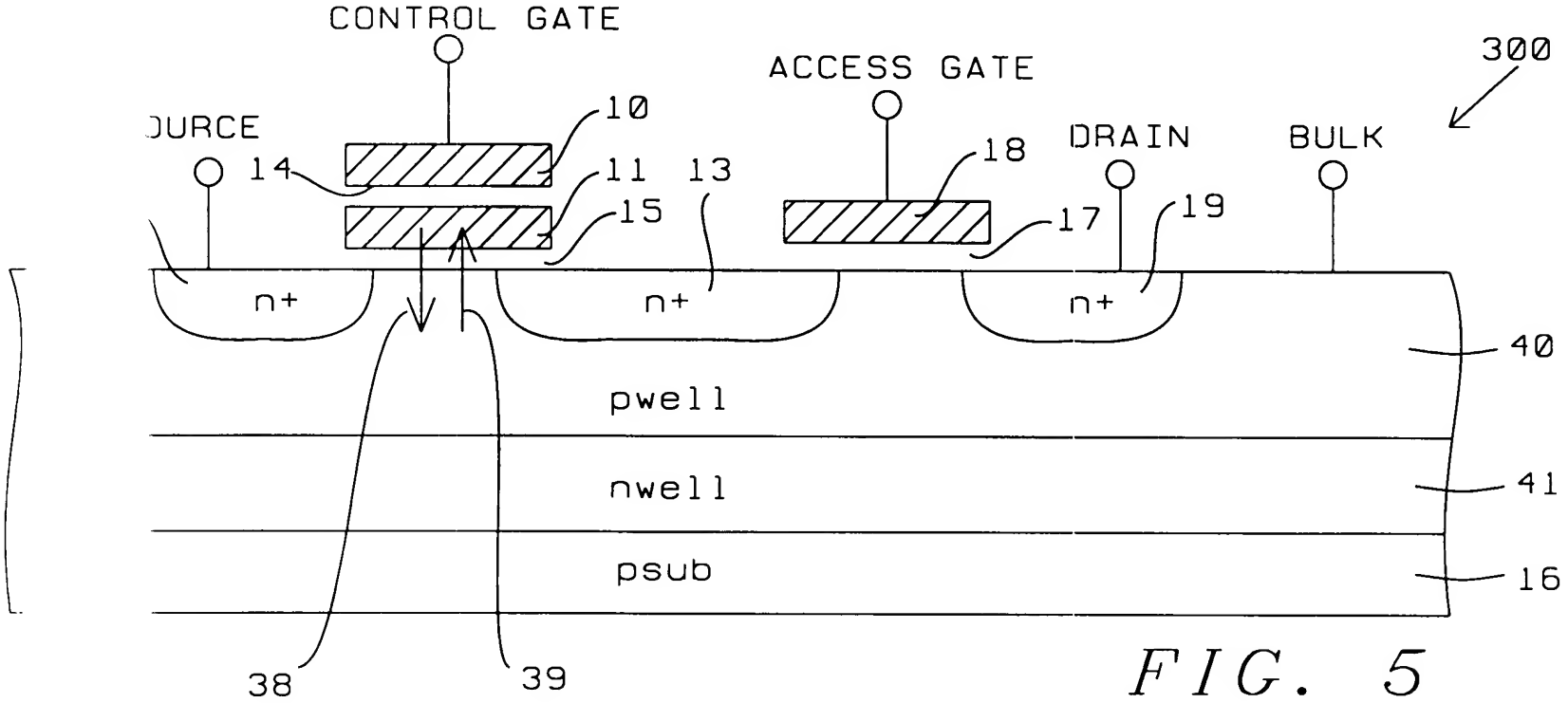

FIG. 5

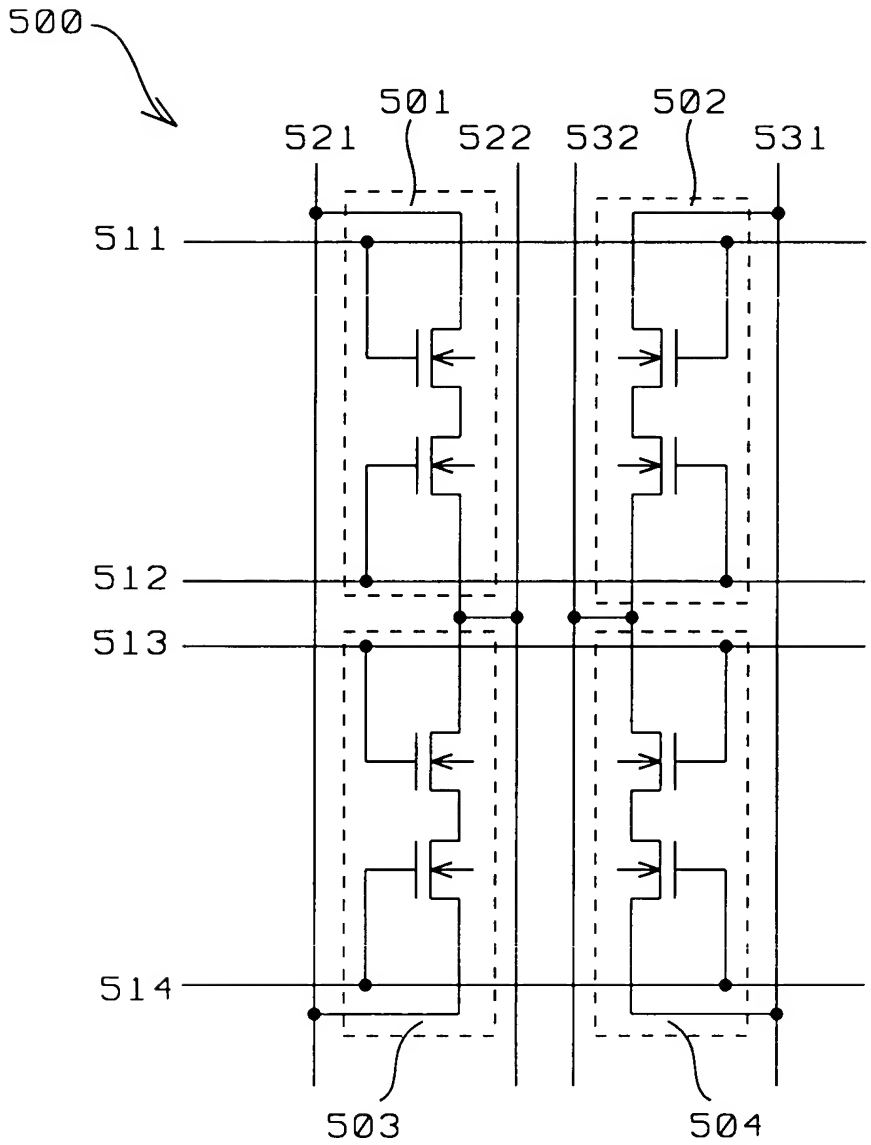

FIG. 6

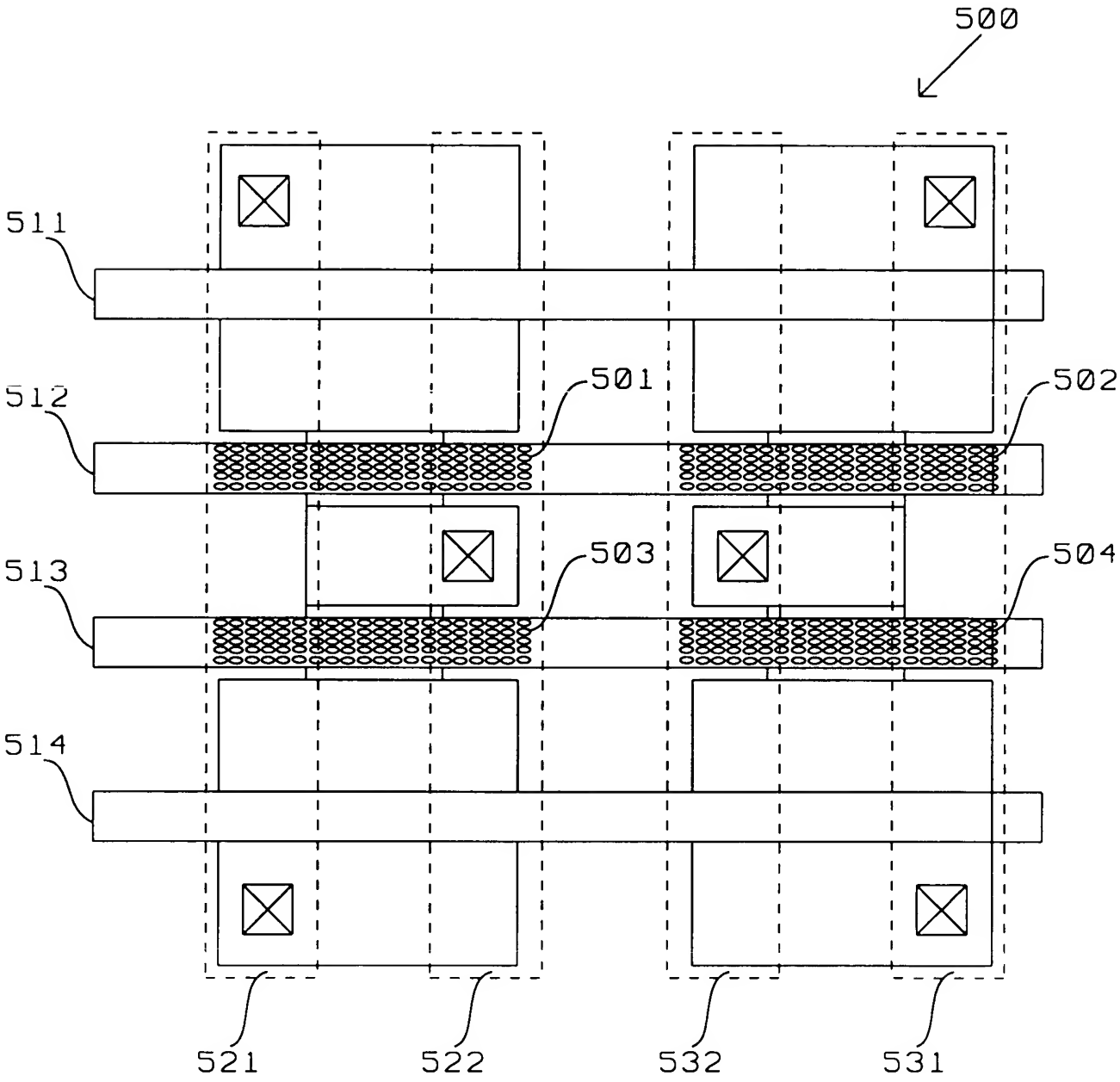

FIG. 7

	Selected WL		Non-Selected WL				
ODE	Vcg	Vag	Vcg	Vag	V_S	V_d	V_{pwell}
RASE	-10 VOLTS	0 VOLT	-	-	5 VOLTS	HIGH Z	5 VOLTS
OGRAM	Vpgm	Vcc	-2.5 VOLTS	Vcc	HIGH Z	-5 VOLTS	-5 VOLTS
OGRAM IBIT	Vpgm	Vcc	-2.5 VOLTS	Vcc	HIGH Z	0 VOLTS	-5 VOLTS
AD	Vcc	Vcc	0 VOLTS	0 VOLTS	0 VOLTS	1 VOLT	0 VOLTS

FIG. 8a

	Selected WL		Non-Selected WL				
ODE	Vcg	Vag	Vcg	Vag	V_S	V_d	V_{psub}
RASE	-15 VOLTS	0 VOLT	-	-	0 VOLTS	HIGH Z	0 VOLTS
GRAM	Vpgm	8 VOLTS	2.5 VOLTS	8 VOLTS	HIGH Z	0 VOLTS	0 VOLTS
GRAM IBIT	Vpgm	8 VOLTS	2.5 VOLTS	8 VOLTS	HIGH Z	5 VOLTS	0 VOLTS
AD	Vcc	Vcc	0 VOLTS	0 VOLTS	0 VOLTS	1 VOLT	0 VOLTS

FIG. 8b